MNEMOSENE

Computation-in-Memory Architecture based on Resistive Devices

MNEMOSENE is a three year project with the aim of demonstrating a new architecture concept together with its required programming flow and interface.

The overall goals of MNEMOSENE will be to:

1. Develop, design and demonstrate the concept of computation-in-memory (CIM) based on resistive-computing using emerging non-volatile memresistive switching devices.

2. Develop and design an efficient compiler, a simulator and FPGA emulator for the new architecture (CIM device combined with a conventional CPU).

www.mnemosene.eu

European Commission