# CLAROZON

## TECHNOLOGIES

signs of innovations

# FINAL YEAR PROJECT CENTER COIMBATORE

Welcome to Clarozon

The mixtures of dynamic professionals have made CLAROZON TECHNOLOGIES standout from the competition.

We have a team dedicated to share and infuse knowledge into Brains.

With subject ideas and experts concentrating on their areas of experience,

we are transforming in a unique way.

# BEST PROJECT CENTER COIMBATORE

- Warm Welcome to Clarozon Technologies,

- we are having a strong team of Engineers who are experienced to help your company to triumphantly achieve its business goals .

- our commitment to quality is our foremost virtue extending not only to dealing with our clients, but even to the candidates we present.

- Clarozon Technologies is one of the fastest growing software development companies and real time project centers in Coimbatore as well.

# COIMBATORE PROJECT CENTER

- We trusting in true Engineering, hereby put forth the proposal to Project Training,

-  latest technology in use by the industries to the students of Diploma and engineering colleges.

- This scheme will definitely help the students in a better way and will act as a stepping-stone for their future studies, placements in industries and companies. Moreover, student of your college lighted of all.

# LIST OF PROJECT CENTER COIMBATORE

- We play a Dual Positive Role by satisfying the academic requirements and as well as giving the necessary training in Software Design and Development which enables the Students to meet the industrial requirements with a wider knowledge and a greater confidence.
- **We offer Projects in following domains**
- **VLSI
  EMBEDDED
  MECHANICAL
  POWER ELECTRONICS
  POWER SYSTEM
  MATLAB
  NS2
  SOFTWARE**

# CLAROZON TECHNOLOGIES

SERVICES:

Our course training department is in charge of offering these organizations an integrated solution of the highest quality for their training needs.CLAROZON academic team is made up of professionals who along with great management career also have had extensive experience in such specific tasks.

- VLSI
- EMBEDDED SYSTEM
- PLC
- PCB DESIGN
- MECHANICAL
- IMPLANT TRAINING

# CLAROZON FINAL YEAR PROJECTS COIMBATORE

## VLSI

- ADVANCED DIGITAL ELECTRONICS

    Digital Concepts and Number Systems and Principles of Combinational Logic Circuits.
    Analysis and Design of Combinational Logics.
    Principles of Sequential Logic Circuits, Counters & Registers.
    Design of Sequential Circuits.
    Introduction Programmable Logic Devices (PLDS).
    Introduction to FPGAs and ASIC Design.
    Introduction to CMOS Circuits.

# CLAROZON FINAL YEAR PROJECT CENTER COIMBATORE

- VHDL LANGUAGE

  Introduction to VLSI Technology
  Identifiers and Literals
  Entities and Architectures
  Packages and Libraries
  Standard Package
  Std_logic_1164 Package
  Textio Package
  Other Packages
  Creating Packages
  Interface Declarations
  Port Declarations
  Generics

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- Type Declarations
  - Other Declarations
  - Objects Declarations
  - Subprogram Declarations
  - Alias and Attribute Declarations
  - Predefined Attribute
  - Sequential Statements
  - Wait Statements
  - Assertion and Reports Statements
  - Assignment Statements
  - Procedure Call
  - If Statement
  - Case Statement
  - Other Sequential Control Statements

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- Operators
  - Arithmetic
  - IEEE Synthesis Packages
  - Concurrent Statements
  - Block Statement
  - Process Statement
  - Concurrent Procedure Call
  - Concurrent Signal Assignments
  - Concurrent Assertion Statement
  - Component Instantiation
  - General Statement
  - Execution
  - Configuration and Specifications
  - Packages
  - Writing Test benches

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- VERILOG HDL LANGUAGE

    Basics of the Verilog Language
    Verilog logic Value
    Verilog Data Types
    Other Wire Types
    Numbers
    Negative Numbers
    Strings
    Operators
    Arithmetic
    Hierarchy
    Procedures and Assignments
    Continuous Assignments statements
    Sequential Block
    Procedure Assignments
    Timing Controls and Delay
    Timing Control

# CLAROZON FINAL YEAR PROJECT CENTER COIMBATORE

- Data Slip
  Wait Statements
  Blocking and Non Blocking Assignments
  Procedural Continuous Assignments
  Task and Functions
  Control Statements
  Case and If Statements
  Loop Statement
  Disable
  Fork and Join
  Logic –Gate Modeling
  Built in logic Models
  User Defined Primitives

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- Modeling Delay
  Net and Gate Delay
  Pin-to-Pin Delay
  Altering Parameters
  Other Verilog Features
  Display Tasks
  File I/O Tasks
  Timescale, Simulation, and Timing-Check Tasks
  PLA Tasks
  Stochastic Analysis Tasks
  Simulation Time Functions
  Conversion Functions
  Probability Distribution Functions
  Programming Language Interface
  Writing Test benches

# CLAROZON FINAL YEAR PROJECT CENTER COIMBATORE

- ADVANCED LOGIC SYNTHESIS USING FPGA
  CMOS LOGIC

  Introduction
  Semiconductor device Physics
  Fabrication of MOSFET
  MOSFET operation and modeling
  MOS inverters - Static characteristics
  MOS inverters - Switching characteristics and interconnect effects.

  FIELD PROGRAMMABLE GATE ARRAYS (FPGAs)

  Introduction to Programmable Logic devices &FPGAs, and Programming
  Technologies
  FPGA Design Flow
  Technology Mapping, Placement & Routing
  FPGA Architectures: Introduction to Xilinx devices, SPARTAN II
  Xilinx Design Flow and Implementation Details, Applications and Case Study on
FPGAs

# CLAROZON FINAL YEAR PROJECT CENTER COIMBATORE

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