

High Spectral Purity Microwave Oscillator: Design Using Conventional Air-Dielectric Cavity

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Abstract—We report exceptionally low PM noise levels from a microwave oscillator that uses a conventional air-dielectric cavity resonator as a frequency discriminator. Our approach is to increase the discriminator's intrinsic signal-to-noise ratio by use of a high-power carrier signal to interrogate an optimally coupled cavity, while the high-level of the carrier is suppressed before the phase detector. We developed and tested an accurate model of the expected PM noise that indicates, among other things, that a conventional air-dielectric resonator of moderate Q will exhibit less discriminator noise in this approach than do more esoteric and expensive dielectric resonators tuned to a high-order, high-Q mode and driven at the dielectric's optimum power.

I. INTRODUCTION

MICROWAVE oscillators of the highest spectral purity usually employ frequency-locking to a high-Q resonance cavity to clean up the broadband phase noise [1]–[6]. The resonance cavity could be a part of the oscillator itself as its frequency-determining element [5], [6] or could be an external one, used only to stabilize the oscillator [2], [4]. In either case, it is used primarily as a discriminator. At microwave frequencies, any enhancement of the low-noise discriminator's sensitivity directly translates into corresponding improvement in the higher free-running oscillator's phase noise.

Several key aspects controlling the cavity discriminator sensitivity have been addressed extensively by earlier work. Among these the most important one consists of increasing the cavity Q. Commonly, high-Q resonators use the whispering-gallery modes in sapphire-loaded cavities (SLC). The unloaded Q varies from 2×10^5 to several million in going from room temperature to cryogenic temperatures [3], [7]. The other key aspect controlling the discriminator sensitivity relates to the degree of suppression of the carrier signal reflected from the cavity, as this reduces the effective noise temperature of the nonlinear mixer, which acts as the phase detector. The amount of carrier suppres-

sion can be increased by making the effective coupling coefficient into the cavity approach its critical value of unity [3] and also by using interferometric signal processing [5], [6]. The SLC resonators are commonly used with effective coupling coefficients near 0.7. Additional use of interferometric suppression can result in an overall carrier suppression of greater than 80 dB [5], [6].

Another important aspect of the discriminator sensitivity is that it scales directly as the power of oscillator signal incident into the cavity. This point has not been the subject of much discussion or experimental investigation. This is because when using an SLC resonator one is faced with the following problem. For a nearly critically coupled cavity, most of the incident microwave power is dissipated in the dielectric material due to high field confinement in a whispering-gallery mode. Due to the dependence of the dielectric permittivity of sapphire on temperature, fluctuations of the dissipated power, caused by amplitude modulation (AM) noise on the signal, give rise to SLC resonance frequency fluctuations. This effect of power-to-frequency conversion results in a discriminator noise floor that scales as the square of the dissipated power [8]. Thus, increasing the power to improve the discriminator sensitivity can become counterproductive in the case of the SLC resonator. Suggested ways around this problem are: 1) frequency-temperature compensation of the sapphire dielectric resonator [9]; 2) suppression of the oscillator AM noise; and 3) controlling the SLC operating temperature by making use of the difference in frequency-temperature coefficients of different modes of the SLC resonator [8]. Frequency-temperature compensation for sapphire has been found to work effectively using paramagnetic compensation with Ti^{3+} -doped Sapphire, dielectric compensation with a composite Sapphire-Rutile structure, or a variety of thermomechanical compensations (see, e.g., references in [9]). However, all of these have been tried out only at cryogenic temperatures. At room temperatures the paramagnetic and dielectric compensations are associated with relatively large loss of resonator Q.

In the present work we describe the design of a cavity-stabilized oscillator (CSO) that uses a discriminator comprised of a room-temperature air-dielectric cavity at 10 GHz to clean up the phase noise of a commercial yttrium iron garnet (YIG) oscillator. Salient features of our design include: 1) modest unloaded cavity Q values

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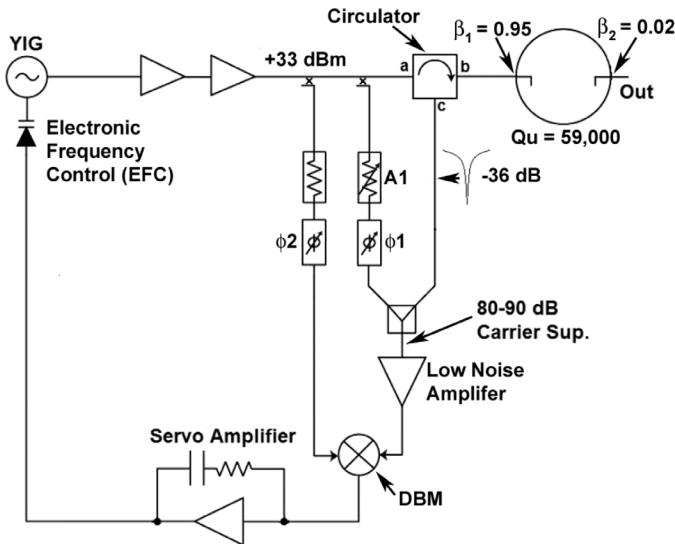


Fig. 1. Schematic diagram of cavity stabilized oscillator (CSO) as used in this paper.

around 50,000–70,000; 2) coupling coefficient of approximately 0.95, achieved using simple coupling loops; 3) interferometric signal processing resulting in an overall carrier suppression of about 90 dB; and 4) incident signal power of +33 dBm. In the following sections we demonstrate by a comprehensive theoretical model of the discriminator noise floor that the loss of sensitivity resulting from the lower values of Q are compensated by the large power level. Phase noise measurements on a fabricated prototype of the CSO, relative to two room-temperature SLC Oscillators in a three-cornered-hat configuration [10] indicate that the approach presented here significantly improves on traditional CSO methodology that was originally developed at NIST [2].

II. DESCRIPTION OF OSCILLATOR

The basic approach in our experimental setup, shown in Fig. 1, consists of cleaning up the phase noise of a YIG oscillator by using a high- Q air-dielectric cavity as a discriminator. The YIG oscillator at 10 GHz is a commercial one that allows voltage tuning over a ± 1 -GHz range. Also, a special design enables very fast tuning with a 3-dB bandwidth of typically 2.5 MHz. Its output power of +13 dBm is amplified to +33 dBm using two stages of amplification. The key element of the discriminator, the microwave cavity, is an air-filled cylindrical cavity operating in one of the higher TE modes. The inside surface of the cylinder and the end plates are highly polished and silver-plated. The dimensions of the cylinder are optimized to yield the highest value of Q . Two cavities were fabricated to operate in TE₀₂₃ and TE₀₂₅ modes, and their unloaded Q values are measured to be 59,000 and 73,000, respectively.

Signal power is coupled into and out of the cavity using small loops on the end plates, with their planes aligned with the radial plane of the cylinder. At this high frequency

the loop diameters are barely over a millimeter. The coupling coefficient can be varied quite easily by just pushing the loops in and out by small amounts. The input probe, which is slightly larger in size, is adjusted to give a coupling coefficient of 0.95. The coupling of the output probe, on the other hand, is kept at only 0.02. Using the standard formulae for the reflection and transmission coefficients at the cavity resonance frequency,

$$S_{11} = \frac{1 - \beta_1 + \beta_2}{1 + \beta_1 + \beta_2} \quad \text{and} \quad (1a)$$

$$S_{21} = \frac{2\sqrt{\beta_1 \cdot \beta_2}}{1 + \beta_1 + \beta_2}, \quad (1b)$$

we get a suppression of the reflected and the transmitted signal out of the cavity by 33 dB and 20 dB, respectively. Considering that our input signal to the cavity has a power of +33 dBm, we get an output power of +13 dBm, which is high enough to be used without any further amplification. The advantage of using the transmitted output is that it results in additional filtering of the broadband noise and spurs. The entire cavity setup is mounted inside a thermally insulated enclosure and its temperature is controlled to within 10 mK.

Further suppression of the reflected signal is carried out using interferometric processing as shown in Fig. 1. Using mechanically variable attenuator A1 and phase shifter ϕ_1 to generate the compensating signal, it is possible to easily achieve further suppression of the reflected signal by 50 dB or more. The variable attenuator and the phase shifters are not kept under as tight temperature control as the cavity. However, this does not impair the carrier suppression settings very significantly over time. After suppression, an input carrier level of about -50 dBm appears as input to the amplifier. This is low enough that it does not effectively contribute any low-frequency flicker noise [5]. The amplifier has a gain of 30 dB and a noise temperature of about 100 K. The amplified output is supplied to one port of the phase detector, a double-balanced mixer (DBM), and the other port is given a reference signal in phase quadrature with the first. Phase quadrature condition can be easily obtained within an error of 1 – 2° using the mechanically variable phase shifter ϕ_2 . We simply use voltage null out of the mixer as our criterion for the phase quadrature between the two inputs. This is because our mixer is a carefully chosen one that has very well matched diodes. This ensures that the YIG oscillator AM noise-to-phase noise conversion is not a significant factor compared to the thermal noise floor.

Finally, the servo loop is completed by amplifying the phase detector output by the servo amplifier and feeding it to the voltage tuning port of the YIG. The servo amplifier is a two-stage integrator with additional high-frequency compensation, to give a unity-gain bandwidth of about 4 MHz. The high-frequency compensation is needed in order to offset the roll-off of the voltage tuning response of the YIG beyond about 1 MHz with a 3 dB point at 2.5 MHz.

III. OSCILLATOR PHASE MODULATION NOISE MODEL

As mentioned earlier, the CSO consists of a YIG oscillator whose output is amplified and applied to a coupling port of the discriminator cavity through a circulator. The reflected signal out of the cavity comes out of the port C of the circulator and is already highly suppressed since the coupling is very nearly critical. A portion of the input signal, adjusted to be of the same amplitude and opposite phase as the reflected signal, is combined with the reflected signal to suppress the carrier still further to about -50 dBm. This constitutes the so-called interferometric signal processing [5]. The highly suppressed signal is then amplified by the low-noise amplifier before being applied to one port of the DBM. As a result of the very high level of carrier suppression, the amplifier output exhibits almost no flicker noise. The DBM acts as a phase detector whose other port has a portion of the input signal adjusted to be in phase quadrature with the reflected signal. By having the amplifier before the mixer, the effective noise contribution from the mixer is dominated by the amplifier gain and becomes relatively insignificant. The phase-detector output is the error signal that tracks the frequency fluctuations of the YIG oscillator relative to the cavity. This is applied to the voltage-control input of the YIG through the servo amplifier to stabilize its frequency.

The reflection coefficient of the signal incident on the cavity is given by

$$\Gamma = S_{11} = \frac{(1 - \beta_e^2 + \Delta\nu'^2) + j(2 \cdot \Delta\nu' \cdot \beta_e)}{(1 + \beta_e)^2 + (\Delta\nu'^2)}, \quad (1c)$$

where

$$\begin{aligned} \beta_e &= \frac{\beta_1}{1 + \beta_2}, \text{ the effective coupling coefficient} \\ \Delta\nu' &= \frac{\nu - \nu_{\text{res}}}{\text{HUB}} = \frac{\Delta\nu}{\text{HUB}}, \text{ and} \\ \text{HUB} &= \frac{\nu_{\text{res}}}{2 \cdot Q_u}, \text{ the half unloaded bandwidth,} \end{aligned}$$

The terms ν , ν_{res} , and Q_u are, respectively, the carrier frequency of the signal, the cavity resonance frequency, and the unloaded cavity Q. Further, if the reflected signal is applied to the RF port of the DBM and a reference signal in phase quadrature to its local oscillator port, we can show that the DBM output is

$$\nu_o = k_d \cdot \sqrt{P_i} \cdot (\text{Im}\Gamma) = k_d \cdot \sqrt{P_i} \cdot \frac{2 \cdot \beta_e}{(1 + \beta_e)} \cdot \frac{1}{\left\{1 + \left(\frac{\Delta\nu}{\text{HLB}}\right)^2\right\}} \cdot \frac{\Delta\nu}{\text{HLB}}, \quad (2)$$

where P_i , k_d , and $\text{HLB} = (1 + \beta_e)$ are, respectively, the incident signal power, DBM conversion gain, and cavity half-loaded bandwidth. The corresponding signal power out of

the DBM is

$$P_0 = k_d^2 \cdot P_i \cdot \frac{4 \cdot \beta_e^2}{(1 + \beta_e)^2} \cdot \frac{1}{\left\{1 + \left(\frac{\Delta\nu}{\text{HLB}}\right)^2\right\}^2} \cdot \left(\frac{\Delta\nu}{\text{HLB}}\right)^2. \quad (3)$$

We further make use of the relationship that connects the phase-noise spectral density $S_\varphi(f)$ as a function of the Fourier frequency f and the corresponding frequency fluctuation $\Delta\nu$ of the signal, whose rms value is denoted by $\Delta\nu_{\text{ref}}$, as [10]

$$S_\varphi(f) = \left(\frac{\Delta\nu_{\text{rms}}}{f}\right)^2 \text{ or } (\Delta\nu_{\text{rms}})^2 = f^2 \cdot S_\varphi(f). \quad (4)$$

We note that if the YIG oscillator is locked to the cavity so that its carrier frequency is ν_{res} , then phase-noise-induced $\Delta\nu$ in (4) has the same meaning as in (3). Thus, combining (3) and (4), we can write the rms power out of the DBM as

$$P_{0\text{rms}} = k_d^2 \cdot P_i \cdot \frac{4 \cdot \beta_e^2}{(1 + \beta_e)^2} \cdot \frac{1}{\left\{1 + \left(\frac{f^2 \cdot S_\varphi(f)}{\text{HLB}^2}\right)\right\}^2} \cdot \left(\frac{f^2 \cdot S_\varphi(f)}{\text{HLB}^2}\right), \quad (5)$$

which can be rewritten simply as

$$P_{0\text{rms}} = k_d^2 \cdot P_i \cdot \frac{4 \cdot \beta_e^2}{(1 + \beta_e)^2} \cdot \left(\frac{f^2 \cdot S_\varphi(f)}{\text{HLB}^2}\right), \quad (6)$$

since $S_\varphi(f) \ll 1$ for a reasonable low-noise oscillator.

To compute the discriminator noise floor, we need to consider the noise power present at the output of the DBM due to different sources. The major source is the thermal noise of the microwave amplifier. As mentioned earlier, the flicker noise of the amplifier is very small since the carrier is very highly suppressed and the noise of the DBM is swamped by the amplifier gain. We can write the noise contribution of the amplifier and other lossy components of the system as

$$N_{\text{amp}} = k_d^2 \cdot k_B \cdot (T_{\text{amp}} + T_0), \quad (7)$$

where T_{amp} is the effective noise temperature of the amplifier, T_0 is the ambient temperature (300 K), and k_B is Boltzmann's constant. The second source of noise is the ferrite circulator, which lies within the discriminator. In absolute terms its phase noise is quite small and can be expressed in the form (for each of its three segments) [6]

$$S_\varphi^{\text{circ}}(f) = -150 - 12 \log_{10}(f) \text{ dBc/Hz}. \quad (8)$$

Referring to Fig. 1, the expression for the noise contributed to the discriminator by the segment a-b of the circulator is similar to that from the oscillator, as given in (6):

$$N_{\text{circ}(a-b)} = k_d^2 \cdot P_i \cdot \frac{4 \cdot \beta_e^2}{(1 + \beta_e)^2} \cdot \left(\frac{f^2 \cdot S_\varphi^{\text{circ}(a-b)}(f)}{\text{HLB}^2}\right). \quad (9)$$

However, the noise contributed by the segment b-c modulates a much suppressed carrier of the signal reflected from the cavity. This can be written as

$$N_{\text{circ}(b-c)} = k_d^2 \cdot P_i \cdot \frac{(1 - \beta_e)^2}{(1 + \beta_e)^2} \cdot S_\varphi^{\text{circ}(b-c)}(f). \quad (10)$$

The third source of noise is the phase shifter, which is used to adjust the phase of the compensating signal to exactly oppose the reflected signal and interferometrically cancel it. To make the cancellation automatic, it is usual to use a voltage-controlled ferrite phase shifter (VCP). Although in our experimental setup we have used only a mechanical phase shifter, which does not produce any significant phase noise, we include the discussion of the VCP for the sake of completeness. The noise model for a typical VCP is [6]

$$S_\varphi^{\text{VCP}}(f) = -147 - 7.5 \log_{10}(f) \text{ dBc/Hz}. \quad (11)$$

The noise contributed by the VCP also modulates the suppressed reflected carrier, and the expression for it is similar to that for the circulator segment b-c. We can write this as

$$N_{\text{VCP}} = k_d^2 \cdot P_i \cdot \frac{(1 - \beta_e)^2}{(1 + \beta_e)^2} \cdot S_\varphi^{\text{VCP}}(f). \quad (12)$$

Thus the noise floor of the discriminator can be computed simply by realizing that it is the smallest signal power out of the DBM, given by (6), which equals the sum of all the noise contributions from different sources, given by (9), (10), and (12):

$$P_{\text{rms}} = N_{\text{amp}} + N_{\text{circ}(a-b)} + N_{\text{circ}(b-c)} + N_{\text{VCP}}. \quad (13)$$

Substituting from (6), (9), (10), and (12), we get the noise floor $S_\varphi^{\text{nf}}(f)$ for the discriminator as that value of $S_\varphi(f)$ in (6) that satisfies (13). This is given by

$$S_\varphi^{\text{nf}}(f) = \frac{k_B(T_{\text{amp}} + T_0)}{P_i} \cdot \frac{(1 + \beta_e)^2}{4\beta_e} \cdot \left(\frac{\text{HLB}}{f}\right)^2 + S_\varphi^{\text{circ}}(f) + \left\{ \frac{(1 - \beta_e)^2}{4\beta_e^2} \cdot \left(\frac{\text{HLB}}{f}\right)^2 \right\} [S_\varphi^{\text{circ}}(f) + S_\varphi^{\text{VCP}}(f)]. \quad (14)$$

The first term in (14) corresponds to the microwave amplifier, and the second and third correspond to the circulator and VCP, respectively.

Before describing the experimental results, we discuss the expected noise performance of the CSO based on our noise model. Computations of the expected noise floor were made using (14) with different parameters in our experimental setup. The parameters considered are: $\nu_{\text{res}} = 10$ GHz; $Q_u = 73,000$ (TE025) and $59,000$ (TE023); $\beta_1 = 0.95$; $\beta_2 = 0.02$; and $P_i = +33$ dBm. We also account for the circulator and the VCP noise. Although a VCP has not been used in our prototype CSO, including it will enable a comparison with the earlier results with

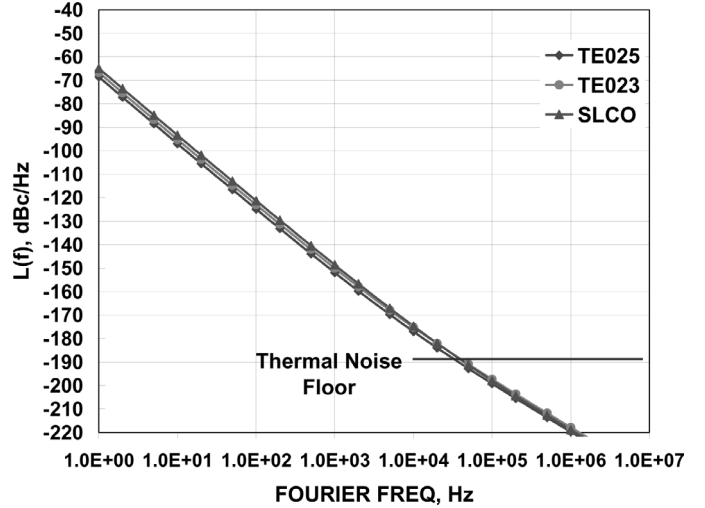


Fig. 2. The computed discriminator noise floors for the CSO using TE023 and TE025 cavities and for the SLCO of Tobar *et al.* [7].

the SLC oscillator (SLCO) [7]. The computed noise floors for the TE023 and TE025 cavities shown in Fig. 2 indicate that results of the two cavities are not markedly different. This is expected since there is a difference of only 20% between their Q_u values. The noise floor initially falls off as f^{-3} and gradually tapers off to an f^{-2} behavior beyond 2 kHz. Beyond 50 kHz the noise floor drops below -190 dBc/Hz, which is the thermal noise floor for the given signal level. Thus we would expect the CSO output to be at the thermal noise level beyond 50 kHz. For the sake of comparison, we have also shown in Fig. 2 the computed noise floor using (14) for the SLCO case by Tobar *et al.* [7]. The parameters considered are: $\nu_{\text{res}} = 10$ GHz; $Q_u = 190,000$; $\beta_1 = 0.75$; $\beta_2 = 0.15$; and $P_i = +17$ dBm, and we also include the noise contributions of the circulator and VCP. We clearly observe that the noise floors in the present results are about 2 to 3 dB lower than for the SLCO throughout the entire range of Fourier frequencies. This is a very significant conclusion, indicating that even using relatively lower-Q air-dielectric cavities it is possible to achieve less discriminator noise than in the SLCO just by driving it with very high signal power. To lend credibility to our model computations, we observe that the experimental results of Tobar *et al.* [7] match the SLCO noise floor in Fig. 2 within a few dB between 10 Hz and 5 kHz. Above this frequency, their observed noise increases due to insufficient servo gain.

Since a VCP has not been used in our experiment, it is of interest to determine the discriminator noise floor by not including its contribution in (14). This is shown in Fig. 3 for the TE023 and TE025 cavities with the same parameters as in Fig. 2, while the SLCO noise floor is shown, as before, for comparison. We observe a significant lowering of the noise floor, which is lowered as much as 7 to 9 dB between 1 kHz and 100 kHz. Also apparent is an f^{-3} slope of the noise floor below 200 Hz, due predominantly to the contribution of the circulator's noise, which can be reduced by making the value of β_1 closer to unity. It can

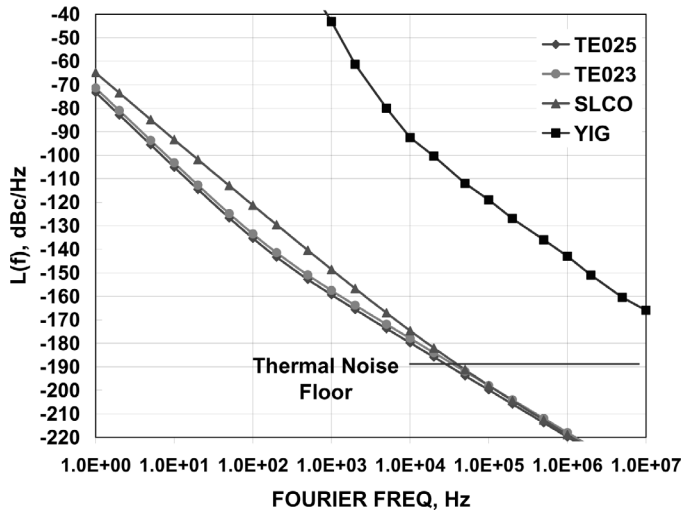


Fig. 3. Computed discriminator noise floors as in Fig. 2, but not including the VCP contribution for the CSO. Also shown is the measured PM noise of the free-running YIG oscillator used in the CSO.

be shown that by making $\beta_1 = 0.98$, there is a reduction of nearly 5 dB at 1 Hz. Above about 200 Hz the noise floor follows an f^{-2} slope as its value becomes directly proportional to the thermal noise of the microwave amplifier and the square of the cavity Q, and inversely proportional to the input power.

IV. MEASURED PHASE MODULATION NOISE OF THE CSO

The phase modulation (PM) noise of the YIG oscillator used in our setup is quite large compared with the discriminator noise floor, which is our goal for the CSO. We have experimentally determined the free-running YIG noise using a delay-line discriminator technique [10] in order to estimate the servo gain needed to bring it down to the discriminator noise floor. These results are also shown in Fig. 3. The need for very high servo gain is clearly apparent. At 10 kHz and 1 kHz the needed gains are, respectively, about 84 dB and 115 dB. Such high gains have been achieved in the CSO by cascading two integrators.

The experimental setup shown in Fig. 4, for the measurement of the PM noise of the CSO, is a cross-correlation setup that uses two commercial SLCOs (Poseidon Scientific Instruments Pty. Ltd., 1/95 Queen Victoria Street, Fremantle WA 6160, Australia) as reference oscillators in a three-cornered-hat configuration [10]. Care was taken to eliminate or minimize the level of cross talk between the three oscillators by using adequate isolation between them. Using the setup in Fig. 4, it was possible to get a measurement noise floor that almost reached the thermal floor near 10 MHz.

Fig. 5 shows the observed PM noise of the CSO and, for comparison, the computed discriminator noise floor using the TE023 cavity. Also shown is the noise floor of the measurement system. Best agreement between the CSO and

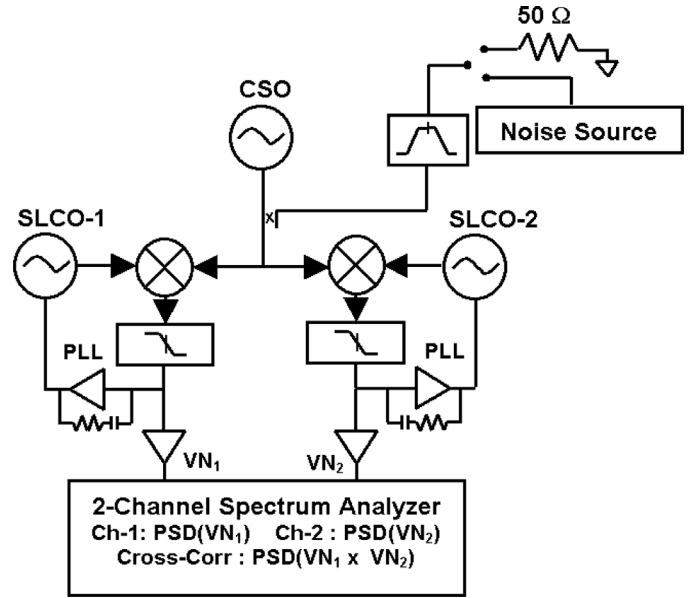


Fig. 4. Cross-correlation three-cornered-hat experimental setup for measurements of PM noise of the CSO.

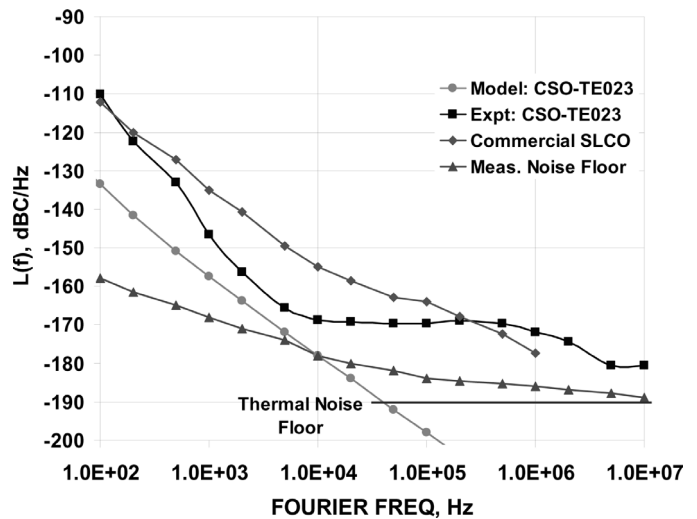


Fig. 5. Measured PM of the CSO using a TE023 cavity. Also shown are the discriminator noise floor as in Fig. 3 and the measured PM noise of an SLCO.

our model computation happens between 1 and 10 kHz, where it is within 6 to 9 dB. At higher Fourier frequencies up to about 1 MHz the disagreement increases because of the gradual roll-off of the integrator in the servo. Beyond 1 MHz the drop in the CSO noise is caused by the band-pass filter action of the cavity. For Fourier frequencies lower than 1 kHz, the PM noise of the CSO begins to deviate by more than 10 dB from the computed discriminator noise, becoming 20 dB worse at 100 Hz. In the low-frequency range, there are two major factors that play a role. First, due to inadequate stability of the temperature controller, the fluctuations of the ν_{res} of the cavity translate into increased PM noise of the CSO. This can be reduced with improved temperature control, which we plan to do in the future.

Second, some spurious low-frequency peaks were apparent in the raw data consisting of both electromagnetic pickup and mechanical resonances, but were narrow enough to readily observe the level of underlying random noise by checking above and below peaks. These narrow-line spectra are not indicated in Fig. 5 since they varied depending on the operating environment and presumably could be reduced with better mechanical mounting and isolation. Finally, we have shown the PM noise of one of the commercial SLCOs that was used in our cross-correlation PM noise-measurement setup. Clearly, even in its preliminary implementation, the CSO exhibits significantly lower noise than the SLCO.

V. FUTURE PROSPECTS

The present results of the low-phase-noise behavior of the CSO employing an air-dielectric over-moded cavity are very encouraging. However, the present work is preliminary and will need to be refined in order to approach the noise predicted by the model computations. In the future, we plan to work further on the following aspects:

1. More rigid mounting and environmental isolation of the cavity.
2. More stable mounting of the coupling loops.
3. Better temperature control.
4. Higher-gain servo with higher-frequency unity-gain point.
5. Choice of higher carrier frequency for the CSO.
6. Use of a 3-dB hybrid in place of the circulator.

VI. SUMMARY

We have described the construction of a CSO that uses a conventional air-dielectric microwave cavity resonator as a frequency discriminator to drastically lower the free-running phase noise of a commercial YIG oscillator. The novelty of our approach is that it aims to offset the disadvantage of a modest cavity Q of about 70,000 (compared to the more esoteric and expensive SLC systems, both at room and cryogenic temperatures) by increasing the carrier power to interrogate an almost critically coupled cavity. We have developed and tested an accurate model of the expected phase noise, which indicates that the CSO performance should indeed compare well with that of the SLCO. Our initial measurements on the first prototype CSO show a phase noise of -105 dBc/Hz at 100 Hz, -145 dBc/Hz at 1 kHz, and -178 dBc/Hz at 1 MHz offset from the carrier. We suspect that the lower-frequency results are contaminated by inadequate environmental isolation, while the high-frequency end is compromised by lack of servo gain. In summary, we have demonstrated that even in its preliminary implementation our new approach of the CSO exhibits a performance comparable to or even exceeding that of the SLCO.

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Dr. Sen Gupta is a Fellow of the National Academy of Sciences, India, and a Senior Member of the IEEE. He has more than 50 research publications in national and international journals.



David A. Howe is leader of the Time and Frequency Metrology Group of the National Institute of Standards and Technology and the Physics Laboratory's Time and Frequency Division. From 1970 to 1973, he was with the Dissemination Research Section where he coordinated the first TV time experiments, from which evolved television closed captioning, as well as lunar-ranging and spacecraft time-synchronization experiments. He worked in the Atomic Standards Section from 1973 to 1984 doing advanced research on cesium and

hydrogen maser standards and ruggedized, compact rubidium oscillators leading to several present manufacturing techniques. He returned to the Dissemination Research Section in 1984 to lead and implement several global high-accuracy satellite-based time-synchronization experiments with other national laboratories. For this contribution, he was awarded the Commerce Department's highest commendation, the Gold Medal, in 1990 for advancements in time calibrations among standards laboratories who participate in the maintenance of UTC. From 1994 to 1999, he worked as a statistical analyst for the Time Scale Section which maintains UTC(NIST) and developed the Total and Theol variances used in high-accuracy estimation of long-term frequency stability. Mr. Howe received physics and math B.A. degrees in 1970 and 1971, respectively, from the University of Colorado and is a member of Sigma Pi Sigma and Phi Beta Kappa academic societies. He avidly enjoys the outdoors with his three children.



Craig Nelson was born in Washington, DC, on February 26, 1967 and grew up in West Germany. Before receiving his BSEE from the University of Colorado in Boulder in 1990, he worked as an undergraduate for NIST's Time and Frequency Division in the development of the electronics systems for NIST-7 Cs atomic frequency standard. After graduation, he worked for Bernoulli Optical Systems and co-developed the write-once-read-many optical disk drive. At the same time, he partnered with another engineer to form SpectraDynam-

ics Company, a supplier of low phase noise components to NIST and other industrial and commercial users. A significant increase in consulting and contracting in several programs at NIST resulted in hiring of Craig by NIST in 1995. He subsequently worked on the control electronics and software for both the NIST-7 and F1 primary frequency standards. He is presently involved in research and development of ultra-stable synthesizers, low phase noise electronics, and phase noise metrology. Current areas of research include high speed pulsed phase noise measurements and phase noise metrology in the 100 GHz range. He has published over 20 papers and frequently presents classes, tutorials, and workshops on the practical aspects of high-resolution phase noise metrology.



Archita Hati was born in W.B., India on April 26, 1970. She received her B.Sc., M.Sc., and Ph.D degrees in Physics from University of Burdwan, W.B., India, in 1990, 1992, and 2001 respectively. She also received her M. Phil. in Microwaves from the same university in 1993. Her Ph.D. work involved studies on high performance frequency synthesizers and related problems.

Since May 2001 she has been a guest researcher with the Time and Frequency Metrology Group of the National Institute of Standards and Technology and the Physics Laboratory's Time and Frequency Division. Her area of extensive experience includes high-precision spectrum and noise analysis of precision oscillators. She was involved with the design of low phase noise Cs frequency synthesizers for atomic clocks. Presently, she is involved in developing a W-band phase noise measurement system. She also performs calibration services for NIST.

She has published more than 25 scientific papers in national and international journals.



Fred L. Walls (A'93-SM'94-F'02) was born in Portland, OR, on October 29, 1940. He received the B.S., M.S., and Ph.D. degrees in Physics from the University of Washington, Seattle, in 1962, 1964, and 1970, respectively. His Ph.D. thesis was on the development of long-term storage and nondestructive detection techniques for electrons stored in Penning traps and the first measurements of the anomalous magnetic ($g-2$) moment of low energy electrons.

From 1970 to 1973, he was a Postdoctoral Fellow at the Joint Institute for Laboratory Astrophysics in Boulder, CO. This work focused on developing techniques for long-term storage and nondestructive detection of fragile atomic ions stored in Penning traps for low energy collision studies. In 1973, he became a staff member of the Time and Frequency Division of the National Institute of Standards and Technology, formerly the National Bureau of Standards in Boulder. He was Leader of the Phase Noise Measurement Group and is engaged in research and development of ultra-stable clocks, crystal-controlled oscillators with improved short- and long-term stability, low-noise microwave oscillators, frequency synthesis from RF to infrared, low-noise frequency measurement systems, and accurate phase and amplitude noise metrology. He has retired from NIST and presently runs Total Frequency in Boulder, CO. He has published more than 150 scientific papers and articles. He holds five patents for inventions in the fields of frequency standards and metrology.

He received the 1995 European "Time and Frequency" Award from the Societe Francaise des Microtechniques et de Chromometric for "outstanding work in the ion storage physics, design and development of passive hydrogen masers, measurements of phase noise in passive resonators, very low noise electronics and phase noise metrology." He is the recipient of the 1995 IEEE Rabi Award for "major contributions to the characterization of noise and other instabilities of local oscillators and their effects on atomic frequency standards" and the 1999 Edward Bennet Rosa Award for "leadership in development and transfer to industry of state-of-the-art standards and methods for measuring spectral purity in electronic systems." He has also received three silver medals from the US Department of Commerce for fundamental advances in high resolution spectroscopy and frequency standards, the development of passive hydrogen masers and the development and application of state-of-the-art standards and methods for spectral purity measurements in electronic systems. Dr. Walls is a Fellow of the American Physical Society, a Fellow of the IEEE, a member of the Technical Program Committee of the IEEE Frequency Control Symposium, and a member of the Scientific Committee of the European Time and Frequency Forum.



Jose Francisco Garcia Nava was born in Manzanillo Colima, Mexico, on October 6, 1967. He received his B.S. EE Instrumentation and Control on January 1990 from Instituto Tecnologico de CD. Guzman Jalisco, Mexico. He also worked in a 300M Watt thermal power plant in Mexico during 1984 to 1987. From 1990 to 1993 he worked for Hermes-Mitsubishi as a group leader of Instrumentation and Control Division. His major responsibilities were the maintenance and construction of thermal power plants, the

design of logic control systems for the Central Termoelectrica Lerma, and stress analysis of the 350M Watt steam turbine for the Central Termoelectrica Tuxpan. In 1994 he joined CENAM (National Center of Metrology) in Queretaro, Mexico. He co-developed the time measurement system for the atomic clocks, as well as the Cs frequency synthesizer. He joined NIST in 1995 where he has been involved in developing high resolution frequency synthesizers, PM/AM noise measurement systems for pulsed amplifiers in X-band and K-band. Presently, he is involved in research for new techniques designed to achieve lower noise in the synthesis of high frequencies as well as being a better father of his lovely daughter Meredith.